



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

re Application of:

Teruyuki MAEDA

Serial No.: 10/735,399

Filed: December 11, 2003

Group Art Unit: 2814

Examiner: PHAM, Long

For: **POWER TRANSISTOR AND SEMICONDUCTOR  
INTEGRATED CIRCUIT USING THE SAME**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

Sir:

In response to the *non-final* Office Action dated July 10, 2006, please amend the  
above-identified application as follows:

A listing of claims begins on page 2 of this paper.

Remarks begin on page 5 of this paper.

Certificate of Express Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service as  
Express Mail, Airbill No. EV 892898218 US, with sufficient postage in an envelope addressed to: Mail  
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November 10, 2006

Date of Deposit

George N. Chaclos  
George N. Chaclos